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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Jae-Jong Han, et al.

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For:

METHOD OF FORMING A GATE ELECTRODE, METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE HAVING THE GATE ELECTRODE, AND METHOD OF OXIDIZING A

SUBSTRATE

INFORMATION DISCLOSURE CITATION FORM PTO-1449 (Modified)

FOREIGN PATENT DOCUMENTS

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